

FIGURE 1A

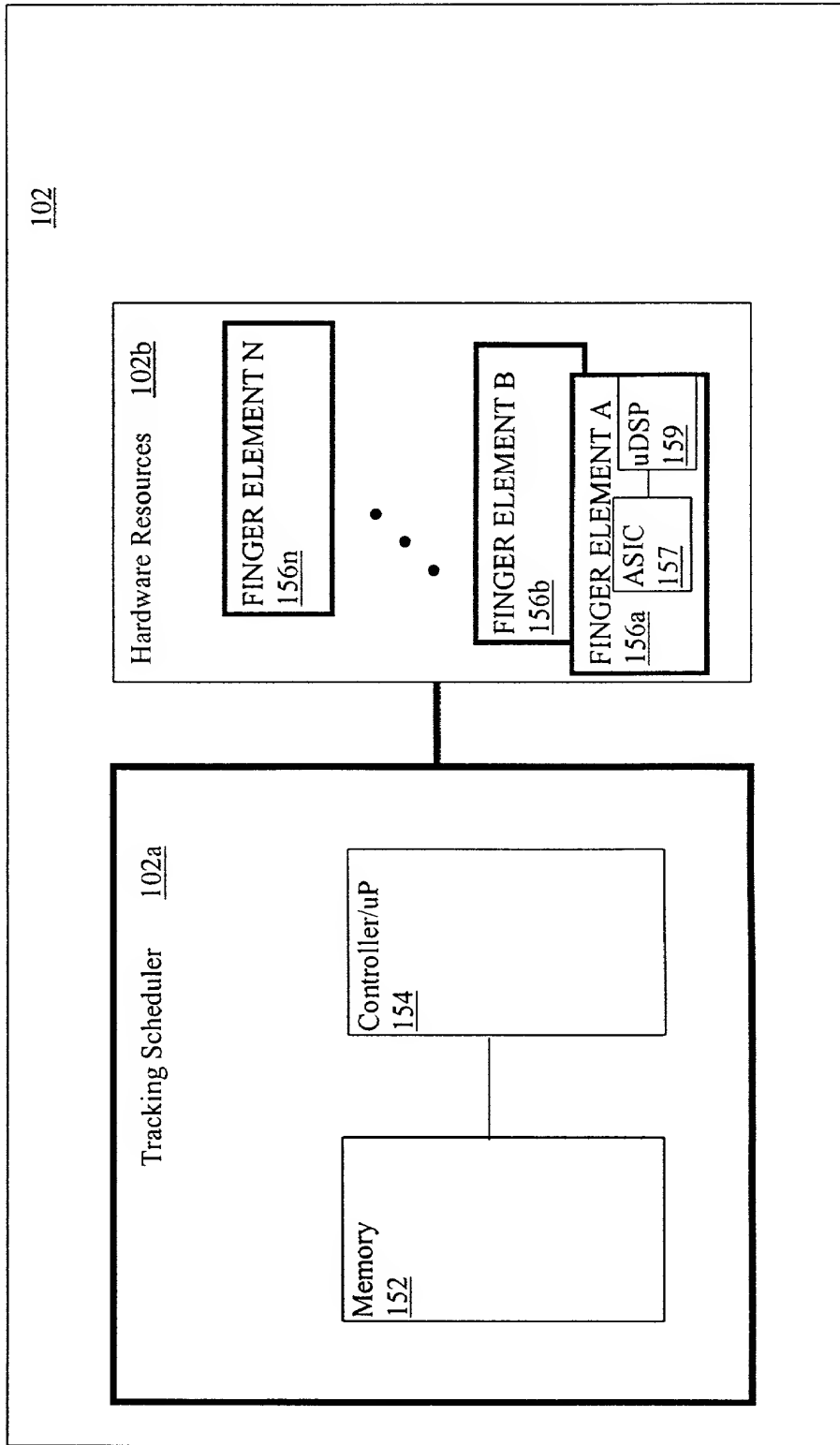


FIGURE 1B

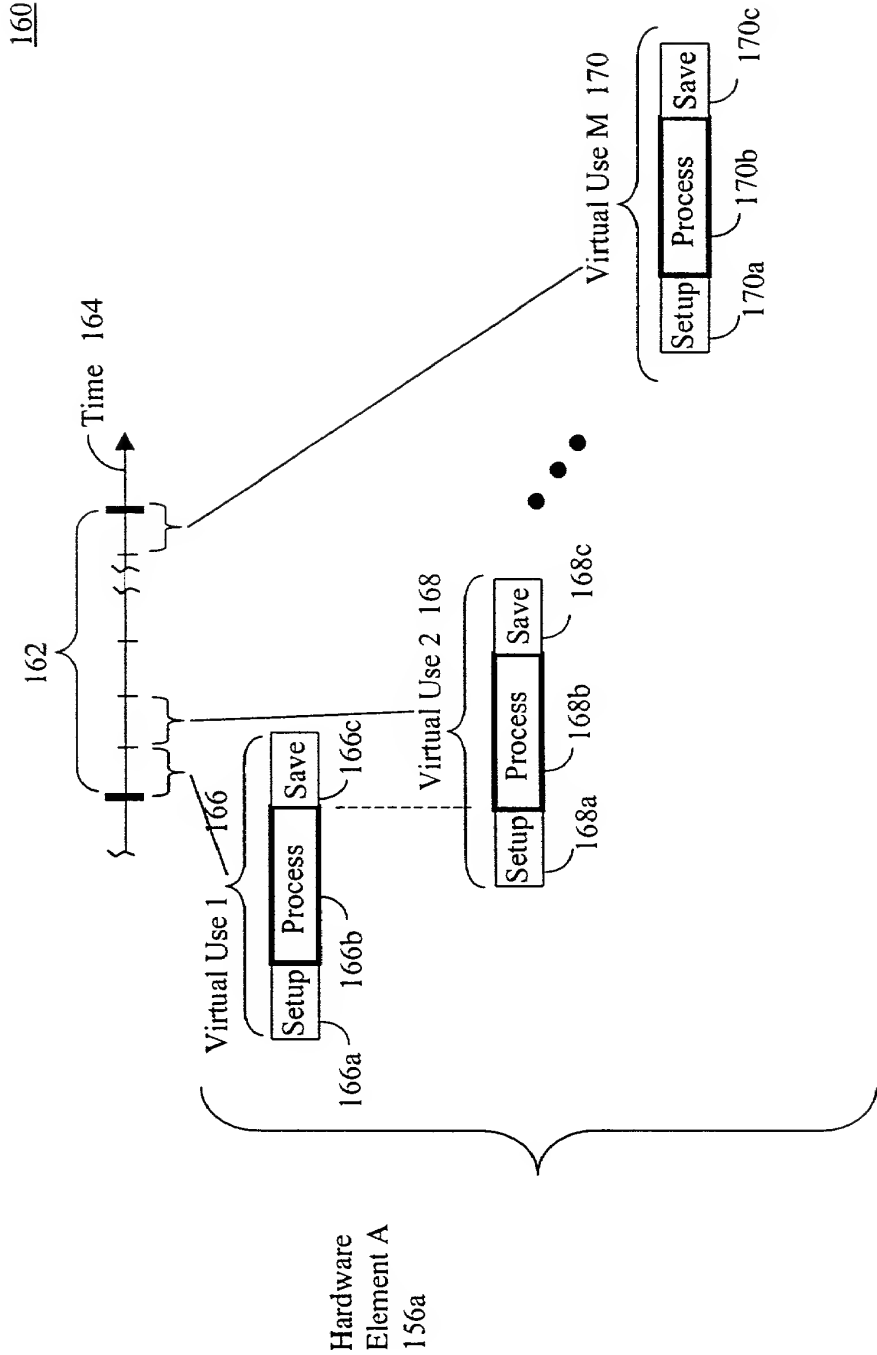


FIGURE 1C

200A

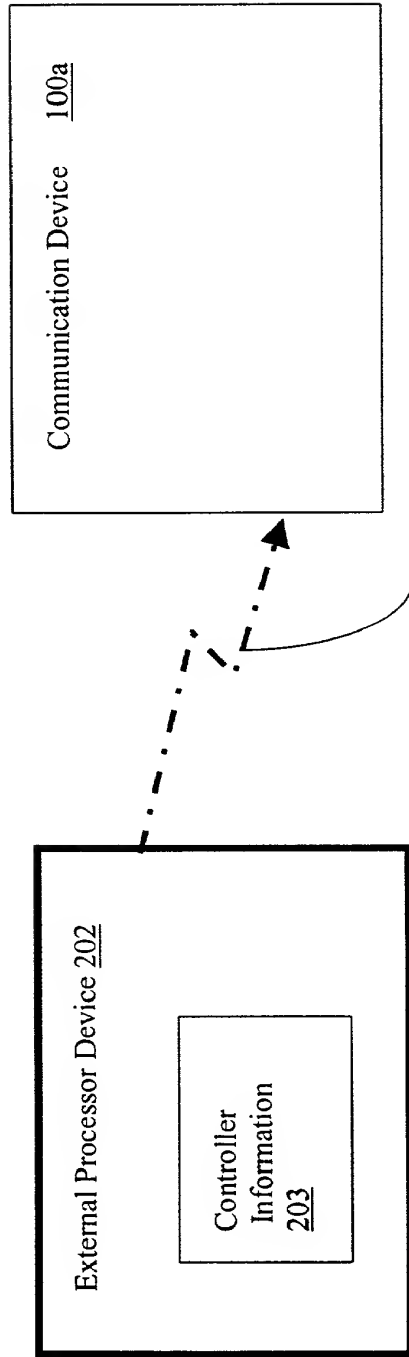


FIGURE 2A

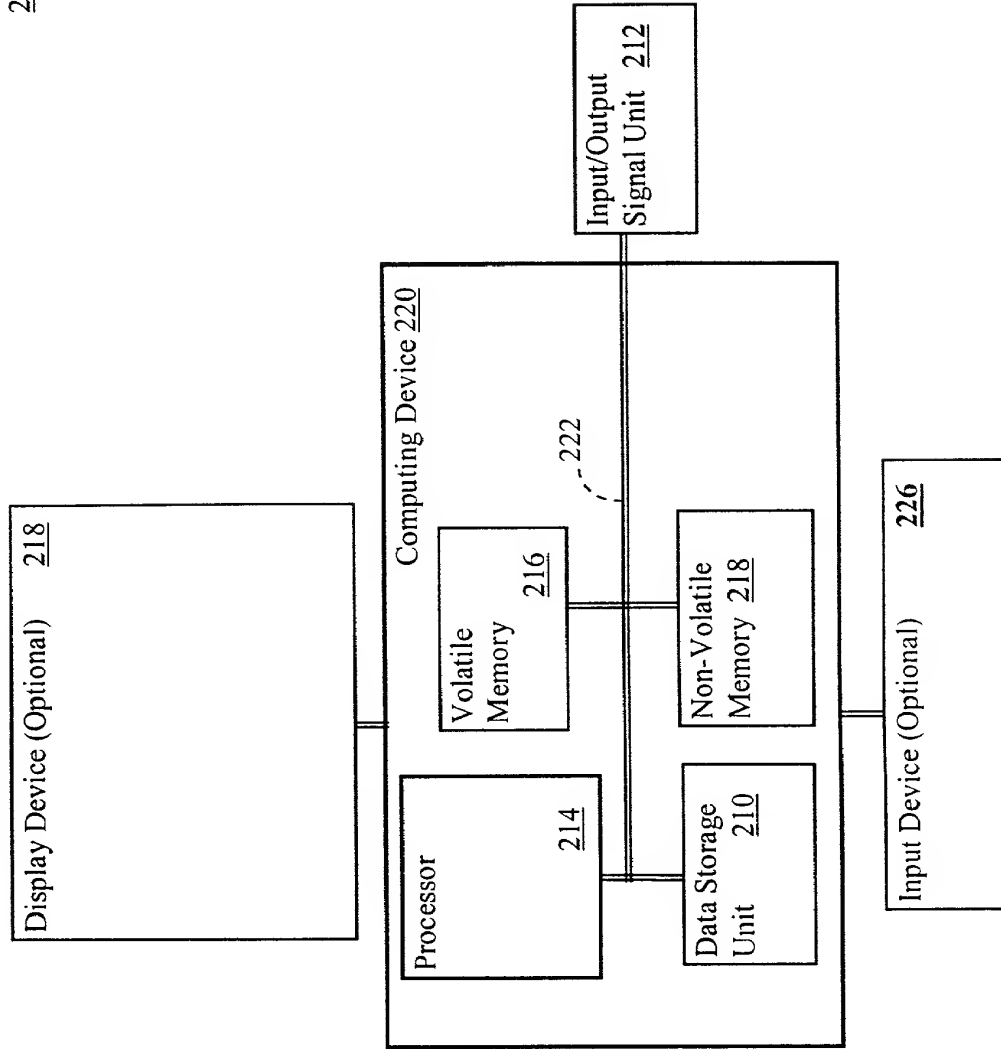


FIGURE 2B

200C

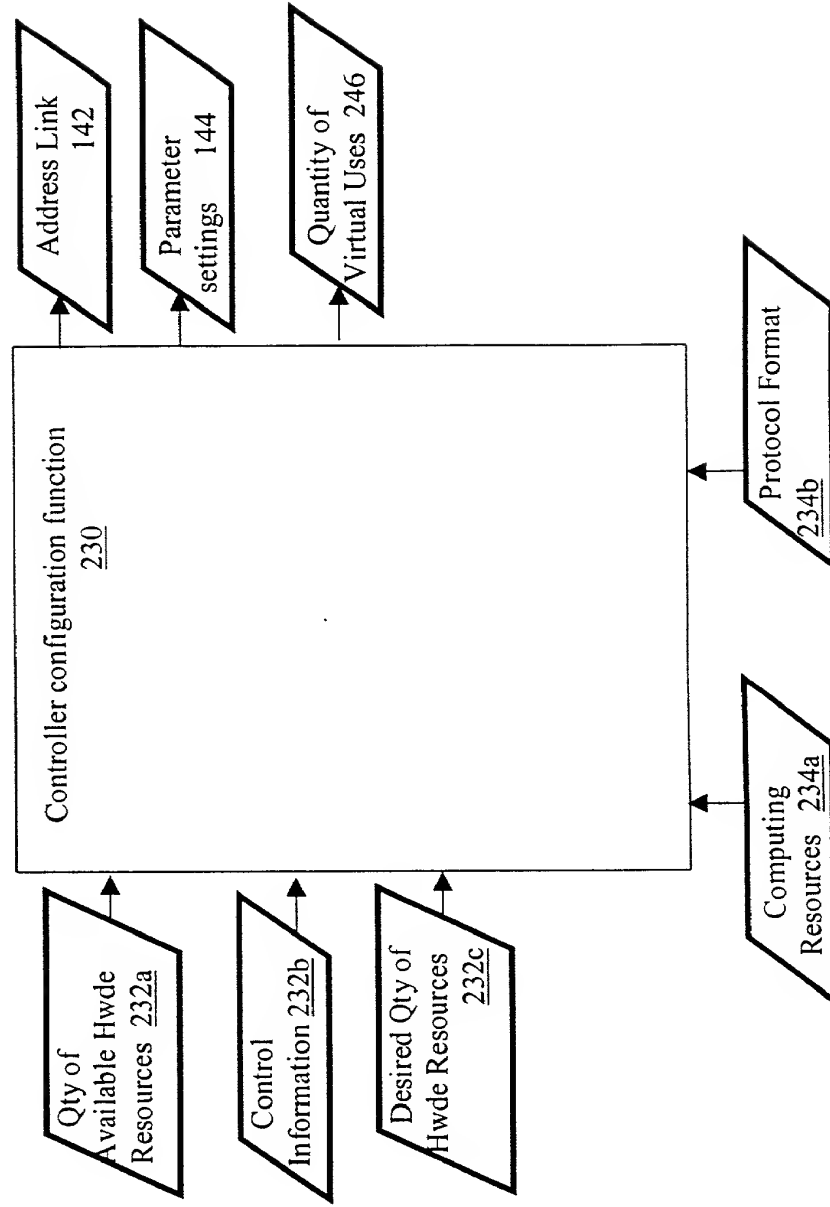


FIGURE 2C

300A
300B

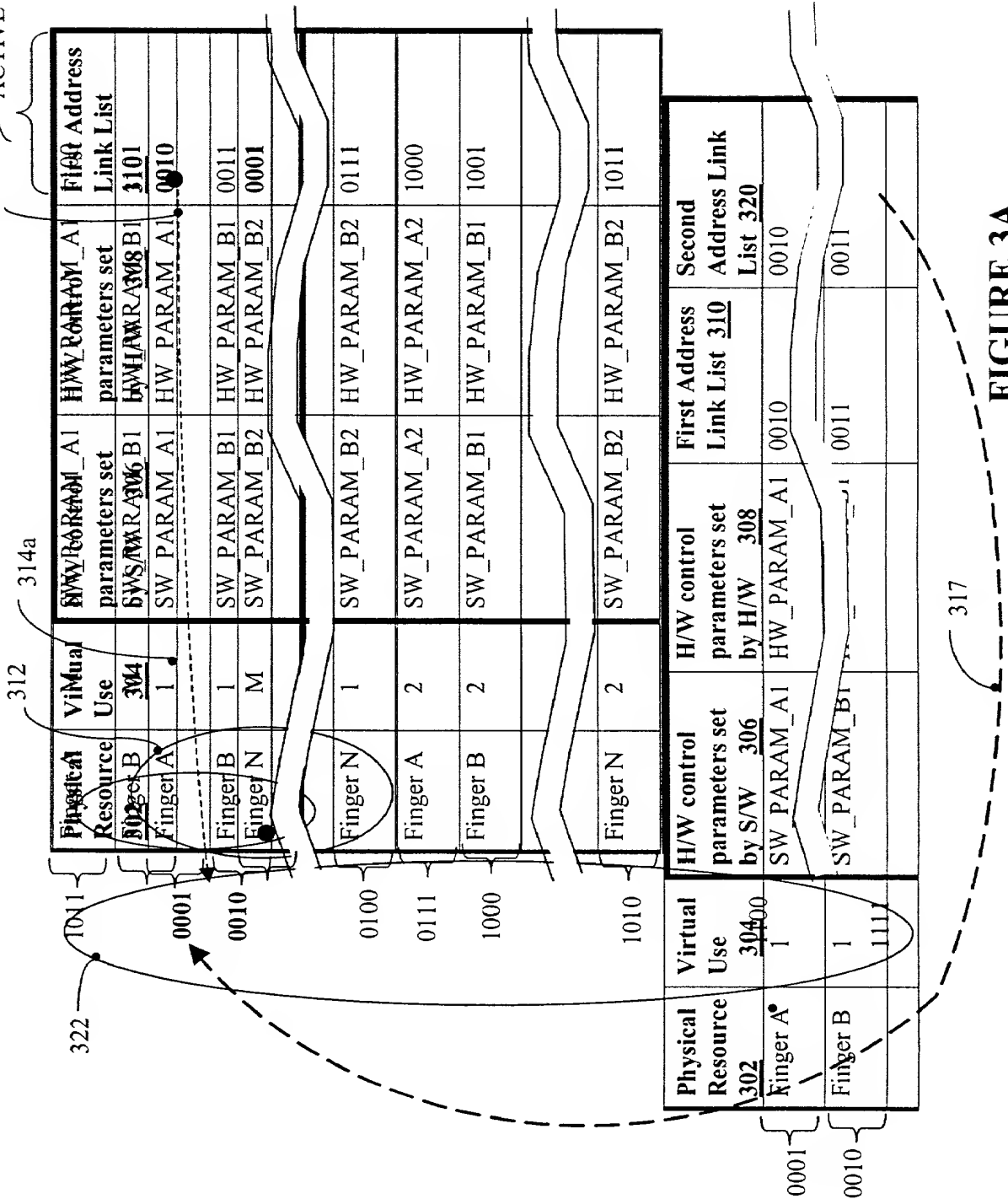
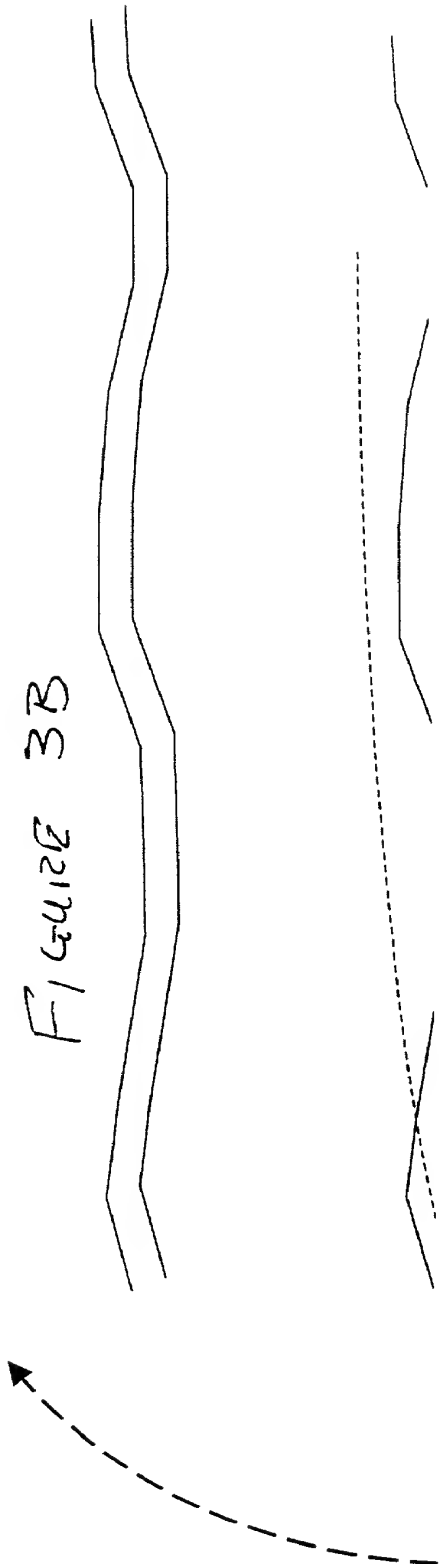


FIGURE 3A

0100	Finger N	1	SW_PARAM_B2	HW_PARAM_B2	0111		0111
0111	Finger A	2	SW_PARAM_A2	HW_PARAM_A2	1000		1000
1000	Finger B	2	SW_PARAM_B1	HW_PARAM_B1	1001		1010
1010	Finger N	2	SW_PARAM_B2	HW_PARAM_B2	1011		1011
1011	Finger A	M	SW_PARAM_A1	HW_PARAM_A1	1100		1100
	Finger B	M	SW_PARAM_B1	HW_PARAM_B1	1101		0001
	Finger N	M	SW_PARAM_B2	HW_PARAM_B2	0001		0001



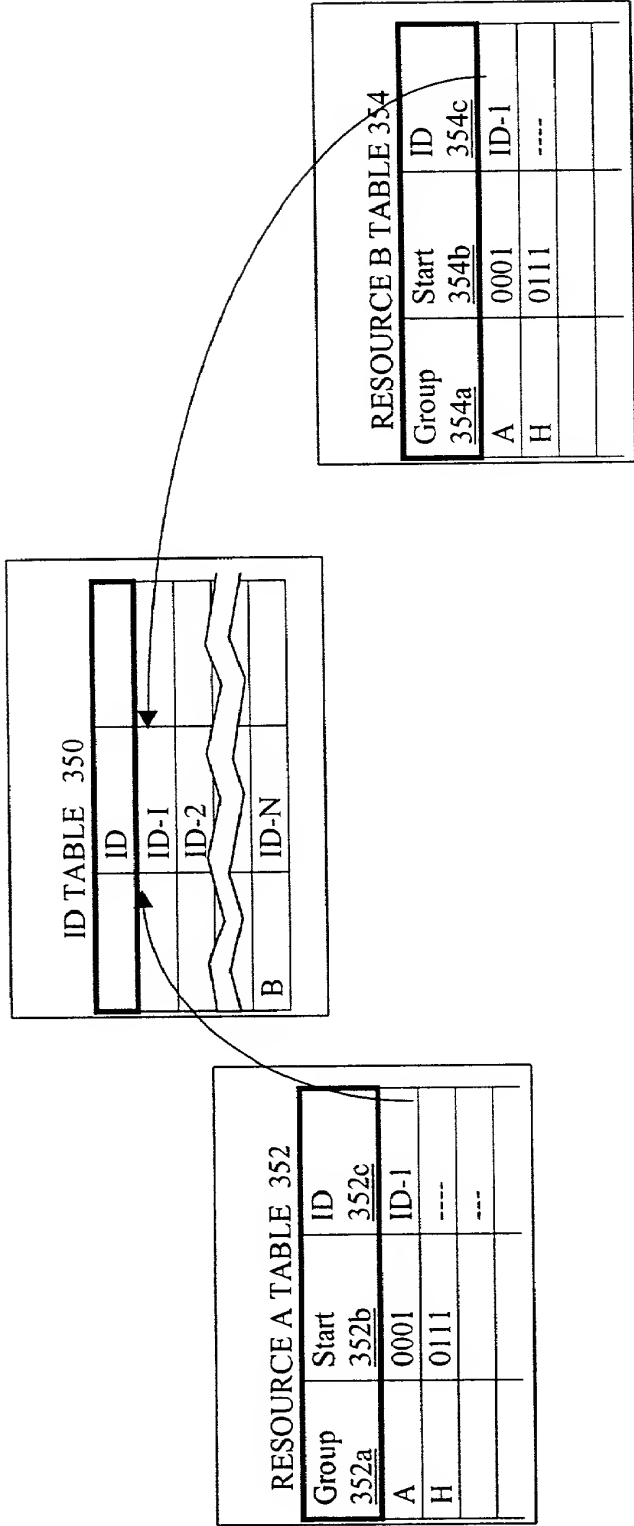


FIGURE 3C

380A

PRIMARY TABLE			
Group 381	Start 382	Return 383	
A	0001	----	
H	0111	----	
B	1011	END	

391

395

SECONDARY TABLE

380B

Physical Resource 384	Time Slot 385	On/Off Flag 386	H/W control parameters set by S/W 387	H/W control parameters set by H/W 388	Return to Master list 389
Elem A	1	ON	SW_PARAM_A1	HW_PARAM_A1	----
Elem B	1	ON	SW_PARAM_B1	HW_PARAM_B1	----
Elem N	1	OFF	SW_PARAM_B2	HW_PARAM_B2	END
Elem A	2	ON	SW_PARAM_A2	HW_PARAM_A2	----
Elem B	2	OFF	SW_PARAM_B1	HW_PARAM_B1	----
Elem N	2	ON	SW_PARAM_B2	HW_PARAM_B2	END
Elem A	M	OFF	SW_PARAM_A1	HW_PARAM_A1	----
Elem B	M	ON	SW_PARAM_B1	HW_PARAM_B1	----
Elem N	M	ON	SW_PARAM_B2	HW_PARAM_B2	END

FIRST =
Block A
370

SECOND =
Block B
372

FIGURE 3D

4000

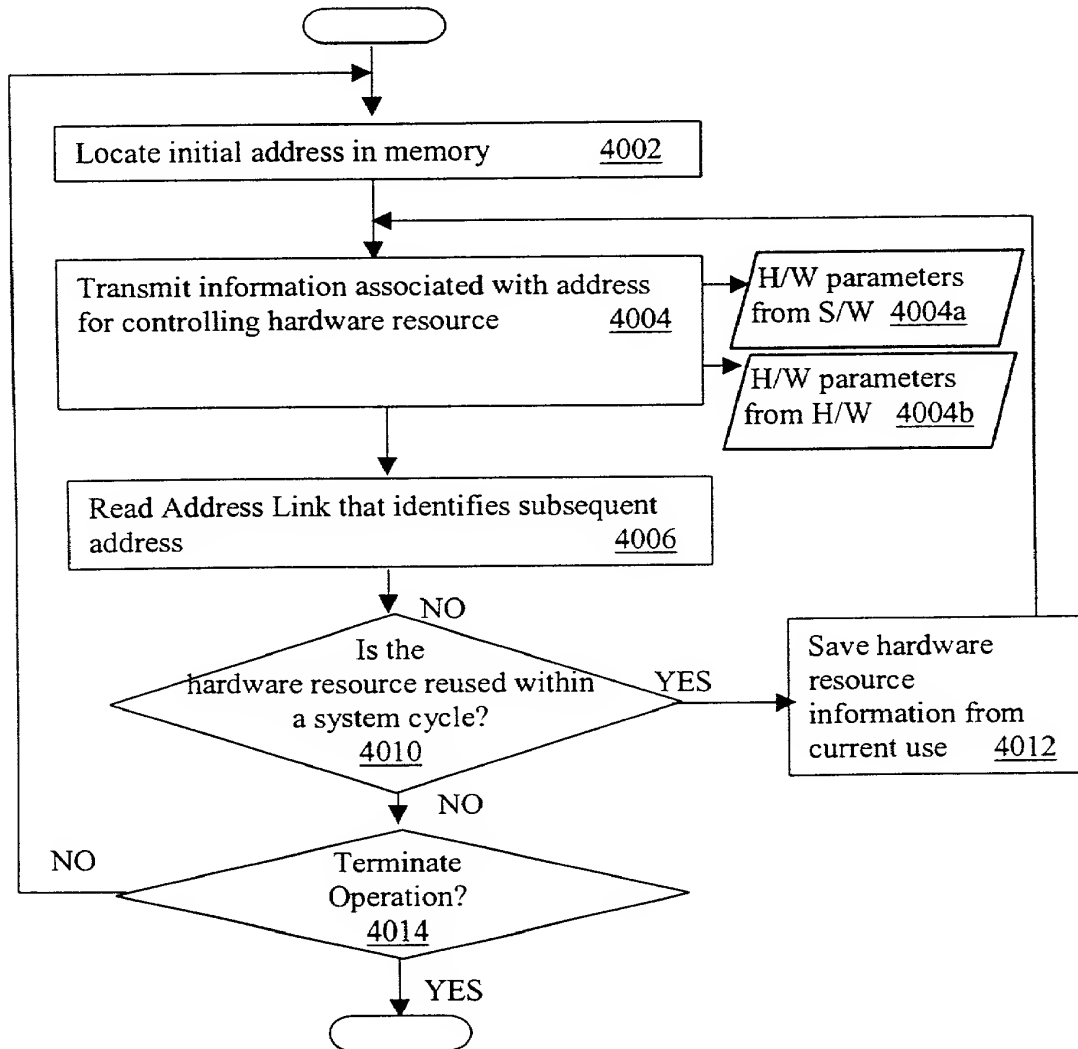


FIGURE 4A

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Atty Docket No: 009824-134-999 Inventor Kavoori et al. Title: Method and Apparatus for Software-Based Allocation and Scheduling of Hardware Resources in an Electronic Device

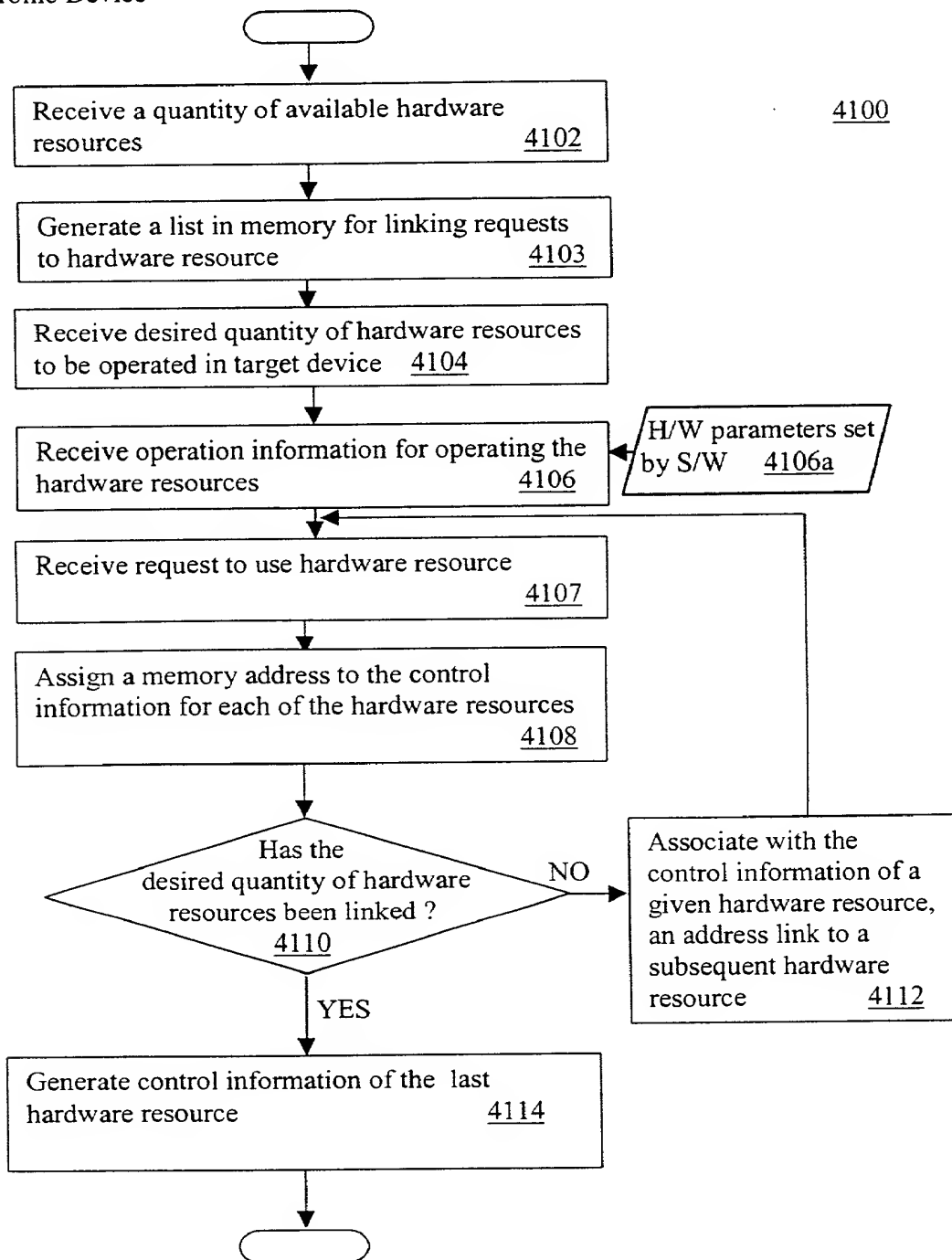


FIGURE 4B

4200

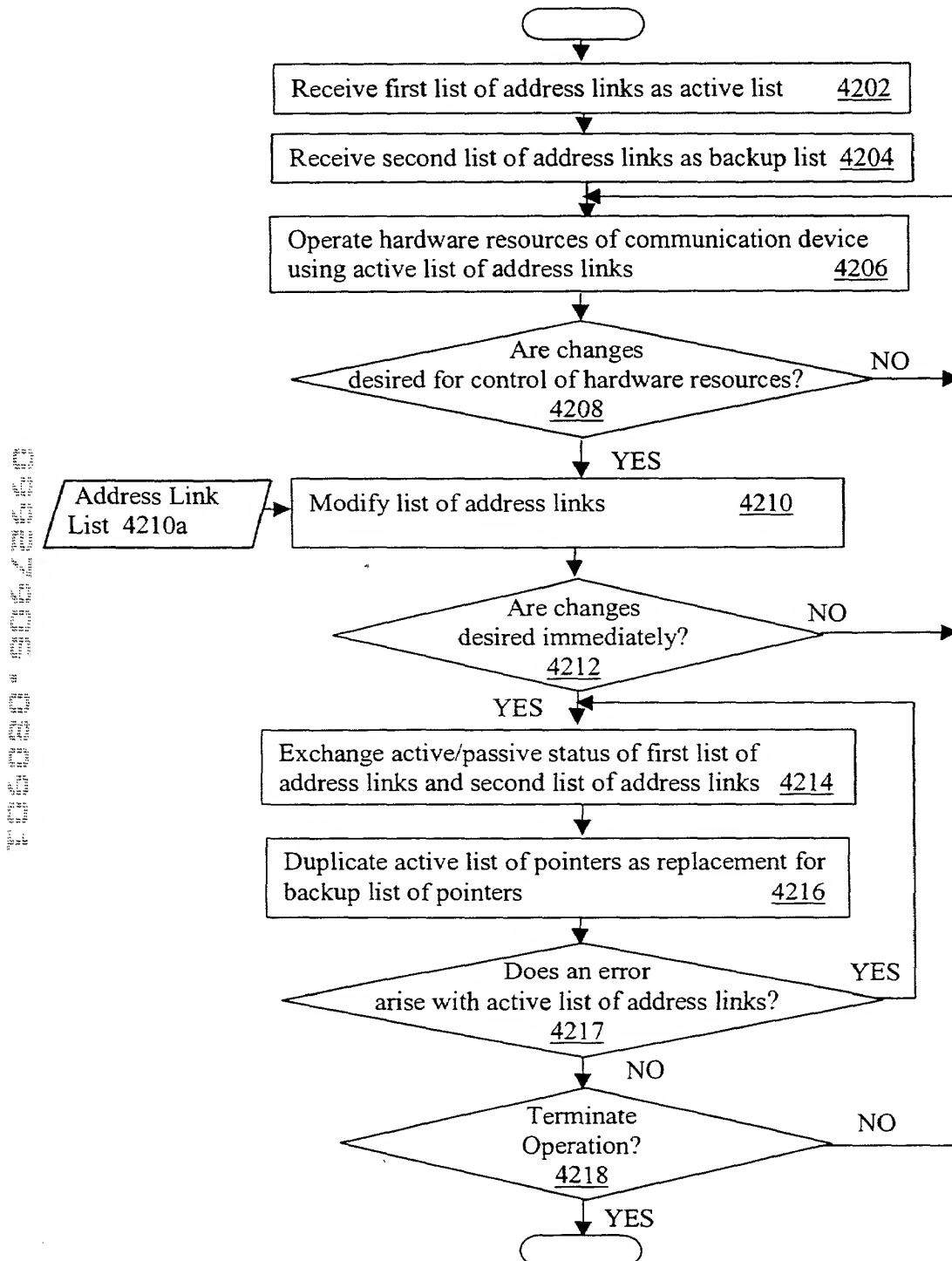


FIGURE 4C